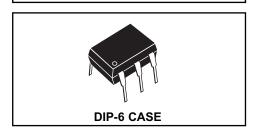


H11C4M

PHOTO SCR OPTOCOUPLER



APPLICATIONS:

- Communications equipment
- · Solid state relays
- Portable equipment
- Low power logic circuits requiring a high degree of input/output isolation

Central [™]
Semiconductor Corp.

DESCRIPTION:

The Central Semiconductor H11C4M is a galliumarsenide infrared emitting Diode optically coupled with a light activated silicon controlled rectifier in a DIP-6 dual-in-line package. This device is designed for applications requiring a high degree of isolation between triggering and load circuits.

MARKING: H11C4M

FEATURES:

- Forward current I_F=50mA MAX (Input)
- On-State current I_{T(RMS)}=150mA (Input)
- Reverse current I_R=10µA MAX (Input)
- $\bullet \ \ \mathsf{Blocking} \ \mathsf{voltage} \ \mathsf{V}_{\mathsf{DRM}}, \ \mathsf{V}_{\mathsf{RRM}} \text{=} 400 \mathsf{V} \ \mathsf{MAX} \ (\mathsf{Output}) \\$
- Isolation test voltage 5000V_{RMS}

3 1 3 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			KIVIS				
MAXIMUM RATINGS: (T _A =25°C)	SYMBOL			UNITS			
Isolation Voltage	V_{ISO}	500	00	V_{RMS}			
Isolation Resistance (@ V _{IO} =500V)	R _{ISO}	10	11	Ω			
Operating Temperature Range	T _J	-30 to	+100	°C			
Storage Temperature Range	T _{stg}	-55 to	+125	°C			
INPUT MAXIMUM RATINGS (LED): (TA=2	25°C) SYMBOL			UNITS			
Continuous Forward Current	l _F	50)	mA			
Continuous Reverse Voltage	V_{R}	5.0	0	V			
Peak Forward Surge Current (tp=1µs)	IFSM	1.0	0	Α			
Power Dissipation	P_{D}	75	5	mW			
OUTPUT MAXIMUM RATINGS (SCR): (TA	(=25°C) SYMBOL			UNITS			
Peak Repetitive Off-State Voltage	V _{DRM} , V _{RRM}	40	0	V			
Peak Reverse Gate Voltage	V_{RGM}	6.0	0	V			
RMS On-State Current	I _T (RMS)	15	0	mA			
Peak On-State Current (tp=100µs)	ΙΤ	10)	Α			
Peak One Cycle Surge (tp=10ms)	ITSM	5.0	0	Α			
INPUT ELECTRICAL CHARACTERISTICS: (T _A =25°C unless otherwise noted)							
SYMBOL TEST CONDITIONS	MIN	TYP	MAX	UNITS			
I_R V_R =3.0 V			10	μΑ			
V _F I _F =10mA			1.5	V			
C_J $V_R=0V$, $f=1.0MHz$		50		pF			

R2 (8-December 2008)



H11C4M

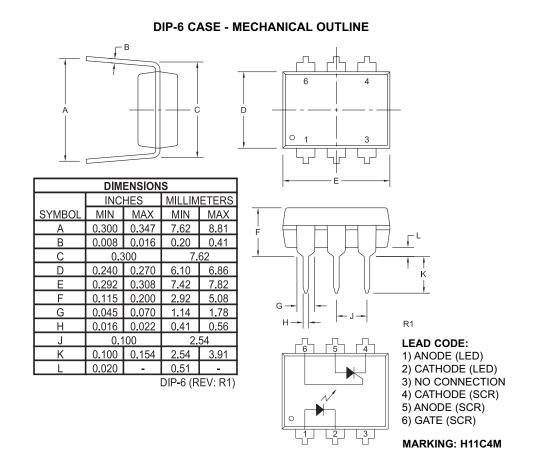
PHOTO SCR OPTOCOUPLER

$\textbf{OUTPUT ELECTRICAL CHARACTERISTICS:} \ (T_{\mbox{\scriptsize A}} = 25^{\circ}\mbox{C unless otherwise noted})$

SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
IDRM, IRRM	V _{DRM} , V _{RRM} =400V, R _{GK} =10kΩ, T _A =100°C		150	μΑ
V_{TM}	I _{TM} =100mA		1.3	V
I _{GT}	$V_D = 100V$, $R_L = 10k\Omega$		50	μA
V _{GT}	$V_D = 100V$, $R_L = 10k\Omega$		1.0	V
I _H	I_T =100mA, R_{GK} =27kΩ		500	μΑ

$\textbf{COUPLER ELECTRICAL CHARACTERISTICS:} \ (T_{\mbox{\scriptsize M}} = 25 \mbox{\ensuremath{}^{\circ}}\mbox{\ensurem$

SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
I _{FT}	V_{AK} =50V, R_{GK} =10k Ω		20	mA
IFT	$V_{\Delta K}$ =100V, R_{GK} =27k Ω		11	mA



R2 (8-December 2008)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- · Package details
- · Application notes
- · Application and design sample kits
- Custom product and package development

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